



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jueng Gil LEE) Examiner: Sikha Roy
)
) Art Unit: 2879
Serial No.: 09/588,115)
) Notice of Allowance dated
Filed: May 31, 2000) August 16, 2004
) Confirmation No.: 7774
)
Title: "STRUCTURE AND METHOD) Our Ref: B-5593 951413-7
FOR FORMING A)
MULTILAYER ELECTRODE...") Date: November 16, 2004

Re: TITLE OF INVENTION

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please note that the title of invention for this application has been amended on October 8, 2003 to "STRUCTURE AND METHOD FOR FORMING A MULTILAYER ELECTRODE FOR A FLAT PANEL DISPLAY DEVICE."

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to:

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
on November 16, 2004

By:


Elizabeth Romero

Respectfully Submitted,



Robert Popa

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